Dhruv Srikanth

dhruvsrikanth5@gmail.com | 646-379-8590 | GitHub | LinkedIn | Website Pittsburgh PA, 15213 The University of Chicago Grad. April 2023 Master of Science, Computer Science (Machine Learning, High Performance Computing), GPA: 3.7 Relevant Coursework: Algorithms, Deep Learning, Machine Learning, CUDA Computing, Times Series, Data Analytics

General Electric

Machine Learning Engineer (Intern) | Aerospace + Healthcare ML

- Reported 4+ leading causes of defects across X-ray insert manufacturing process through ML data analysis with Pandas and sklearn
- Developed-deployed X-ray defect identification model (84% accuracy) with Random Forest & Naïve Bayes classifier (QA pipeline) •

PROJECTS

PyNN (Deep Learning Framework) [code], GAN Experimentation Packages [code], Neural style transfer CLI tool [code], RTConcierge; Road trip recommendations via LLMs, Speech Transcription & Translation [code], GoLLUM - A Compiler between C++ and Go [code]

EDUCATION

Toyota Technological Institute Chicago Joint with UChicago Master of Science. Computer Science	Grad. April 2023
Relevant Coursework: Machine Learning Algorithms, Computational Learning Theory [paper], Deep Learning [pa	aper] [code], Fairness
PES University Bachelor of Technology, Electronics & Communication Engineering, Dean's List (2017-2021), GPA: 3.9 Relevant Coursework: Computer Vision, GPU Design, Machine Learning, Image Processing, Video Processing, Si	<i>Grad. Jun 2021</i> ignals Analysis
Programming Languages: C/C++, GoLang, Python, Rust, Java, MATLAB, SQL, JavaScript, Haskell, R, Mojo, E	Bash
Frameworks: CUDA, PyTorch (& Lightning), TensorFlow, Hugging Face, Ray, Langchain, Kubernetes, MPI, Op	enMP, ElasticSearch
Libraries: NumPy, Pandas, Open AI API, Hadoop, Scikit-learn, Matplotlib, Keras, OpenCV, React, Django, Dock	er, Node, Apache Spark
OPEN SOURCE & PUBLICATIONS	
 PyTorch Contributor Parallel Module Map (with GPU support) Commit Implemented Python-like map modular to functions and inputs for distributed parallel computing across va Enabled developers to distribute training through model-based parallelism with fine-grained control templa TSFresh Contributor 	Pittsburgh, PA Nov 2023 riable number of GPUs ttes Pittsburgh, PA
 Multiprocessing Job Manager (with CPU thread support) Commit Implementing fine-grained control over multiprocessing of parallelized functions Delivered object classes that enabled developers to configure number of processes/jobs in model training a 	June 2023 nd feature extraction
 BIFURC: Bifurcation Identification For Ultrasound-Based Robot Cannulation Cecilia Morales, Dhruv Srikanth, Keith Dufendach, Artur Dubrawski Submitted ICRA Developed AI models and graph-based algorithms for semantic segmentation and identification of vessel b ML for blood vessel surgery in animal subjects Conducted experiments with autonomous robotic arm, proving that method generalizes from phantom-simu 	Pittsburgh, PA Aug 2023 ifurcations, leveraging ulators to pig subjects
 Resource-Conscious High-Performance Models for 2D-to-3D Single View Reconstruction Suraj Bidnur, Dhruv Srikanth, Sanjeev Gurugopinath IEEE Region 10 Conference [paper] [code] Architected 2 novel deep learning models for 2D-to-3D single view reconstruction Leveraged residual and dense layer connectivity, to reduce computational overhead by 20%, improving 	Bangalore, India Dec 2021 state of the art by 25%+
WORK EXPERIENCE	
 Auton Lab – Carnegie Mellon University, Robotics Institute Machine Learning Research Engineer Dr. Artur Dubrawski Trained and fine-tuned large vision and language models over distributed clusters (transformers, VAEs, a Trained multimodal foundation models for detecting patient vitals through drones deployed during disast Published AutonFeat [code] [docs]; distributed automatic featurization library for time series analysis (fore Developed RNNs, LSTMs, transformers with 0.81 AUC with UPMC doctors, improving renal failure out 	Pittsburgh , PA May 2023 – Present utoencoders, bayes nets) er relief ccast + classification) tcomes for ICU patients
 UChicago Booth Center for Applied Artificial Intelligence Machine Learning Researcher Dr. Sendhil Mullainathan Trained 52 models for identifying, measuring and mitigating algorithmic and architectural bias in ML mod Empirically proved presence of inductive biases (induced correlations between covariates – e.g. race, gende (ImageNet) weights and ubiquitous large vision model architectures (AlexNet, VGG, ResNet, DenseNet, V Developed recommendation engine, expert system and API that utilizes user context to modify dynamic known 	Chicago , <i>IL</i> Apr 2022 – Mar 2023 els via transfer learning er) in pretrained 'IT, Diffusion models) towledge graphs
 Myelin Foundry Machine Learning Engineer (Intern) Generative AI, Language Modeling, Continual Learning Created and led revenue stream of \$1m+ USD with real-time competitor analysis tool with LLM & Gene Developed and deployed full stack web applications with Python, MySQL, HTML, CSS, JavaScript, Flask 	Bangalore, India Jan 2021 – July 2021 erative AI framework and Azure VMs
General Electric	Bangalore, India

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June 2019 - Aug 2019